

ABSTRACT

Disclosed here is a method for improving drivability and reduce power consumption in such a display device as a plasma display device. An address electrode driving circuit provided in a plasma display panel display device includes a driving pulse generation circuit, as well as a plurality of address electrode driving parts. In each of the address electrode driving parts, a latch latches a preceding pulse output from another latch, then inputs the pulse to an exclusive OR circuit together with a new pulse output from the other latch. A NAND-circuit outputs a drive pulse/ACL only when those pulses change. Consequently, when a shift register outputs signals without changing a signal level, for example, from a High signal to a High signal or from a Low signal to a Low signal, no drive pulse/ACL is output, thereby wasteful drive current consumption is prevented.